

ABSTRACT

A semiconductor apparatus comprises serially-connected bodies comprised of a switch element and a resistance element
5 respectively interposed between terminals adjacent to one another, conduction-test terminals connected to one and another ends of a series of the serially-connected bodies, and a switch control terminal for collectively controlling all the plural switch elements. Also comprised are switch
10 elements interposed alternately on the first semiconductor chip side and the second semiconductor chip side between wires adjacent to one another, conduction-test terminals connected to one and another ends of a series of the serially-connected switch elements, and a switch control terminal for collectively
15 controlling all the plural switch elements.